About PACT
The purpose of PACT 2016 is to bring together researchers from architecture, compilers, applications and languages to present and discuss innovative research of common interest. PACT started as a Data Flow Workshop in conjunction with ISCA 1989 but quickly evolved into a unique venue at the intersection of classical parallel architecture and compilers.
Looking forward to the evolving field we would like to widen the scope of this 25th anniversary edition of PACT to include modern aspects of related technologies such as, but not limited to, relevant aspects of machine learning, data analytics and computational biology.
PACT solicits novel papers, workshops, tutorials and entries to an ACM student research competition on a broad range of topics that include, but are not limited to:

- Parallel architectures and computational models
- Compilers and tools for parallel computer systems
- Multicore, multithreaded, superscalar and VLIW architectures
- Compiler/hardware support for dealing with memory latencies
- Support for correctness in concurrent hardware and software
- Reconfigurable parallel computing
- Dynamic translation and optimization
- I/O issues in parallel computing and their relation to applications
- Parallel programming languages, algorithms and applications
- Middleware and run time system support for parallel computing
- Application-specific parallel systems
- Applications and experimental systems studies of parallel processing
- Relevant aspects of distributed computing and mobile computing
- Heterogeneous systems using various types of accelerators
- Insights from modern parallel applications such as, but not limited to, machine learning, data analytics and computational biology that can influence the design of future parallel architectures and compilers

For more information:
http://pactconf.org/